



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Danial S. Dean

**Serial No.:** 10/633,956

**Filed:** August 4, 2003

**For:** DEVICE FOR MARGIN TESTING A SEMICONDUCTOR MEMORY BY APPLYING A STRESSING VOLTAGE SIMULTANEOUSLY TO COMPLEMENTARY AND TRUE DIGIT LINES

**Examiner:** S. Mai

**Group Art Unit:** 2818

**Attorney Docket No.:** 2269-3318.6US  
(97-0527.06/US)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 26, 2004  
Date

Signature

Deidra Pfeil

Name (Type/Print)

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action of December 15, 2003 whose initial period of response is set to expire on March 15, 2004.

A listing of the **claims** begins on page 2 of this paper.

**Remarks/Arguments** begin on page 3 of this paper.